

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Robert Chau et al.

Serial No.: Not yet assigned

Filed: Herewith

For: A METHOD FOR MAKING A
SEMICONDUCTOR DEVICE HAVING A
HIGH-K GATE DIELECTRIC

Which is a Continuation of Application of:

Serial No: 10/082,530

Filed: February 22, 2002

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Examiner: Not yet assigned

Art Unit: Not yet assigned

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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☐ A statement pursuant to 37 C.F.R. §1.97(e) or

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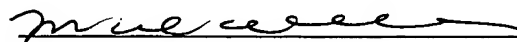
- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7/11, 2003



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